

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENTEES: Katoh
PATENT NO: 6,999,115
ISSUED: February 14, 2006
FOR: IMAGE TAKING APPARATUS WITH AN A/D CONVERTER
AND DSP WITH VARIABLE QUANTIZATION BIT
NUMBERS
GROUP: 2615
EXAMINER: David L. Ometz DOCKET: NEC DP-689

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR CERTIFICATE OF CORRECTION

Dear Sirs:

Satoshi Katoh, the Patentee of the above-identified patent, through his attorney, hereby petitions for issuance of a Certificate of Correction in the above identified patent. A Certificate of Correction (PTO form 1050) is enclosed. The Certificate of Correction is required to correct significant printing errors occurring in Claim 10 and Claim 14 as follows:

Claim 10, Col. 21, line 28, "system controller" should be --a system controller--.

Claim 14, Col. 22, line 37, "plural A/D converting" should be --plural A/D
converting --.

Enclosed are copies of pages 12 and 15 of Amendment B showing the proper claims.

Since the errors were a Patent Office error, it is believed that the Certificate of Correction should be issued without charge to the Applicant.

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Respectfully submitted,



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CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this paper is being deposited with the United States Patent Office
via the electronic filing procedure on June 11, 2009 at
Tucson, Arizona.



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CERTIFICATE OF CORRECTION

PATENT NO. : 6,999,115

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APPLICATION NO. : 09/707,815

ISSUE DATE : February 14, 2006

INVENTOR(S) : Katoh

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 10, Col. 21, line 27, "system controller" should be --a system controller --.

Claim 14, Col. 22, line 37, "plural A/D converting" should be --plural A/D converting --.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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wherein[[:]] said A/D converter sets said quantization bit number based on said bit number converting signal outputted from said system controller.

Claim 6 (previously presented): An image taking apparatus in accordance with claim 5, wherein said system controller, in case that said digital video signals stored in said recording medium are displayed on said displaying apparatus, stops operation of said solid state image taking device, said A/D converter, and said DSP.

Claim 7 (previously presented): An image taking apparatus in accordance with claim 5, wherein said mode setting switch, in case that said digital video signals have been stored in said recording medium, selects whether said digital video signals stored in said recording medium are made to display on said displaying apparatus or not.

Claim 8 (previously presented): An image taking apparatus in accordance with claim 1, further comprising a displaying apparatus driver for making said digital video signals display on said displaying apparatus by thinning out a part of said digital video signals outputted from said DSP.

Claim 9 (previously presented): An image taking apparatus in accordance with claim 1, wherein said image taking apparatus is an electronic still camera.

Claim 10 (currently amended): An image taking apparatus, comprising:
a solid state image taking device which converts an optical image of a subject to be taken to analog video signals and outputs said analog video signals;

a system controller that generates a bit number converting signal;

an analog to digital (A/D) converter which ~~converts at~~ receives said bit number converting signal and generates a designated quantization bit number, said designated

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switching circuit, and also generates said bit number converting signal for setting said signal processing bit number at said DSP based on said operation mode set at said mode setting switch, and outputs said bit number converting signal to said DSP, and

wherein said switching circuit selects either one of said plural A/D converting sections based on said AD converting section changing signal outputted from said system controller.

Claim 15 (previously presented): An image taking apparatus in accordance with claim 14, wherein said system controller, when said digital video signals stored in said recording medium are displayed on said displaying apparatus, stops operation of said solid state image taking device, said A/D converter, and said DSP.

Claim 16 (previously presented): An image taking apparatus in accordance with claim 14, wherein said mode setting switch, when said digital video signals have been stored in said recording medium, selects whether said digital video signals stored in said recording medium are made to display on said displaying apparatus or not.

Claim 17 (previously presented): An image taking apparatus in accordance with claim 10, further comprising a displaying apparatus driver for making said digital video signals display on said displaying apparatus by thinning out a part of said digital video signals outputted from said DSP.

Claim 18 (previously presented): An image taking apparatus in accordance with claim 10, wherein said image taking apparatus is an electronic still camera.

Claim 19 (previously presented): An image taking apparatus, comprising:
a solid state image taking device which converts an optical image of a subject to be taken to analog video signals and outputs said analog video signals;

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